

INFORMATION CITED BY APPLICANT(S) THAT MAY BE MATERIAL TO THE
PROSECUTION OF THE SUBJECT APPLICATION

Applicants: S. Jayaram et al.

Attorney Docket No. WSUR114741

Application No.: 09/888,055

Group Art Unit: 2121

Filed: June 21, 2001

Examiner: --

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Technology Center 2100

VIRTUAL ASSEMBLY DESIGN ENVIRONMENT

U.S. PATENT DOCUMENTS

*Examiner Initial	ID	Document No.	Date	Name
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None

FOREIGN PATENT DOCUMENTS

*Examiner Initial	ID	Document No.	Date	Country	Translation Provided
					Yes No

None

OTHER INFORMATION

(Including Author, Title, Date, Pertinent Pages, Etc.)

*Examiner Initials	Cite No.	
<u>Clear</u>	O1	Jayaram, S., et al., "A Virtual Assembly Design Environment," <i>Proceedings of Virtual Reality</i> , IEEE Computer Society, Houston, March 13-17, 1999, pp. 172-179.
<u>Clear</u>	O2	Jayaram, S., et al., "Virtual Assembly Using Virtual Reality Techniques," <i>Computer-Aided Design</i> 29(8):575-584, August 1, 1997.
<u>Open</u>	O3	Xiaobu, Y., and Hanqiu Sun, "Mechanical Assembly With Data Glove Devices" <i>Proceedings of Canadian Conference on Electrical and Computer Engineering</i> , IEEE, New York, 1997, pp. 177-180.

Examiner

Date Considered

Michael D. Rookney

7/8/04

*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. RCT:cff